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High-performance silicon nanowire bipolar phototransistors

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Silicon nanowires (SiNWs) have emerged as sensitive absorbing materials for photodetection at wavelengths ranging from ultraviolet (UV) to the near infrared. Most of the reports on SiNW photodetectors are based on photoconductor, photodiode, or field-effect transistor device structures. These SiNW devices each have their own advantages and trade-offs in optical gain, response time, operating voltage, and dark current noise. Here, we report on the experimental realization of single SiNW bipolar phototransistors on silicon-on-insulator substrates. Our SiNW devices are based on bipolar transistor structures with an optically injected base region and are fabricated using CMOS-compatible processes. The experimentally measured optoelectronic characteristics of the SiNW phototransistors are in good agreement with simulation results. The SiNW phototransistors exhibit significantly enhanced response to UV and visible light, compared with typical Si p-i-n photodiodes. The near infrared responsivities of the SiNW phototransistors are comparable to those of Si avalanche photodiodes but are achieved at much lower operating voltages. Compared with other reported SiNW photodetectors as well as conventional bulk Si photodiodes and phototransistors, the SiNW phototransistors in this work demonstrate the combined advantages of high gain, high photosresponse, low dark current, and low operating voltage. Published by AIP Publishing. [http://dx.doi.org/10.1063/1.4959264]

The miniaturization of semiconductor devices to the subwavelength scale has opened up new possibilities for creating faster, smaller, and more sensitive photodetectors. In recent years, there has been a growing interest in nanowire photodetectors based on Si,1–3 Ge,10,11 and various III–V12–14 and II–VI1,15 materials. In the simplest device structure, both ends of a single nanowire are connected to electrodes. High photoconductive gains have been reported in such devices and attributed to the trapping of photogenerated charge carriers by nanowire surface states.4,16 On the downside, the performance of nanowire photconductive devices is typically limited by a slow response time of >1 μs and high dark currents in the nA range. Alternatively, nanowire avalanche photodiodes have shown potential for high optical gain and high-speed response1,2 although they typically require a high operating voltage and exhibit excess noise due to the avalanche multiplication process. In bulk devices, bipolar phototransistors (BPT) commonly based on III–V heterojunction materials are capable of providing high optical gain without the drawback of avalanche excess noise.17 However, the large base-collector capacitance in conventional bulk bipolar phototransistors imposes a limit on their high-frequency response.

In this letter, we present the experimental demonstration of a single silicon nanowires (SiNW) photodetector based on the bipolar phototransistor structure. Compared with SiNW photodetectors based on photoconductors or p-n (or p-i-n) junction photodiode structures,1–5 our device structure has the simultaneous advantages of high optical gain, low operating voltage, and lower dark current. An obvious strength of nanoscale bipolar phototransistors over their bulk counterparts is that the significantly reduced parasitic capacitance will result in better high-frequency performance. Moreover, the planar SiNW bipolar phototransistor device structure allows for integration with nanoscale antennas to exploit the properties of surface plasmons for photodetection sensitivity enhancement.10,18–20

We fabricate single SiNW bipolar phototransistors on a silicon-on-insulator (SOI) wafer in a top-down, CMOS-compatible approach (as described in supplementary material Section S1).21 Although a higher emitter injection efficiency can be expected in nnp-type phototransistors, a pnp-type structure based on a p-type SOI wafer is implemented in this work due to the availability of suitable starting wafers. Fig. 1(a) shows a scanning electron microscopy (SEM) image of a single SiNW phototransistor device after formation of the active regions by ion implantation. The design values of the length, width, and thickness of the SiNW are 3 μm, 200 nm, and 200 nm, respectively. SEM inspection reveals that the actual SiNW length and thickness meet the design values, while the actual width is ~250 nm. Along the length of the nanowire, a pnp device structure is defined with a 1 μm p⁺-doped emitter, an n⁺-doped base with length ranging from 100 to 500 nm, a collector with 1 μm unintentionally doped (p⁻) section followed by a p⁺-doped sub-collector with length <1 μm. Each end of the nanowire is connected to a large-area p⁺-doped contact pad, on which ohmic metal contact is deposited. The desired p⁺ (~5 × 10¹⁹ cm⁻³) and n (~5 × 10¹⁷ cm⁻³) doping levels in the device regions are achieved by boron and...
phosphorus ion implantation, respectively, as confirmed by secondary ion mass spectroscopy (SIMS) (see supplementary material, Figure S1.3).\(^{21}\) The shallow SiO\(_2\) trenches underneath the emitter and sub-collector regions in Fig. 1(a) are imprints caused by the faster etch rate of the boron-implanted SiO\(_2\) window regions by buffered oxide etchant (BOE). The optical microscopy image of an array of these devices is shown in Fig. 1(b).

The photocurrent characteristics of the SiNW phototransistors are measured under illumination by a fiber-coupled beam from a diode-pumped solid-state laser (CNI laser MLL-FN-532-400 mW) with a wavelength of 532 nm. The light intensity is varied over four decades by passing the laser beam through neutral density filters. The incident beam intensity is found to be fairly uniform across an area that is over five orders of magnitude larger than the top surface of the SiNW. A commercial photodiode (Hamamatsu G10899-003K) with validated responsivity is used to measure the incident light intensity. Figs. 2(a) and 2(b) compare the measured and simulated photocurrent characteristics. Details regarding these simulations and key parameters are described in supplementary material Section S2.\(^{21}\)

It is evident from Fig. 2(a) that the photoresponse of the SiNW phototransistor scales with light intensity over four orders of magnitude, from 3 mW/cm\(^2\) to 30 W/cm\(^2\). At high light intensity (30 W/cm\(^2\)), the devices exhibit a high photocurrent density of over 250 A/cm\(^2\) with good reproducibility. In Fig. 2(b), the measured and simulated photocurrent curves at an illumination intensity of 15 W/cm\(^2\) are plotted on a linear scale for a detailed examination. It can be seen from Fig. 2(b) that the SiNW phototransistor device behaves as theoretically predicted and exhibits a weak Early effect at an operating voltage \(V_E \geq 0.5\) V, where a further increase in \(V_E\) does not result in significant change in the effective base length and photocurrent gain.

The measured dark current level in Fig. 2(a) is below the lower detection limit of our pico-ammeter (100 fA), suggesting that the surface leakage is negligible. For our silicon nanowire devices, this corresponds to a surface recombination velocity (SRV) of \(\leq 1000\) cm/s,\(^{22,23}\) which is assumed in the simulation. It is also assumed that the electron and hole mobilities in the base and collector regions are an order of magnitude lower than those of intrinsic silicon due to surface roughness scattering\(^{12}\) and that the carrier mobilities in the highly p\(^+\)-doped regions are limited by ionized impurity scattering. The carrier mobility values that we assume are within the range of typically low mobility values reported for SiNWs.\(^{24,25}\) A comparison between the experimental data (photocurrent vs. light intensity) of a set of SiNW phototransistors with nominal base lengths ranging from 100 to 500 nm with simulations reveals that the actual base lengths are consistently shorter than their nominal values by approximately 60 nm. For example, Figs. 2(a) and 2(b) show that the simulation results for a device with a base length of 140 nm are in good agreement with the experimental data for a device with a nominal base length of 200 nm. The narrowing of the base length is possibly caused by diffusion of p-type dopants from the emitter (or other impurities) into the

FIG. 1. (a) SEM image of a SiNW \(pnp\) phototransistor device structure after ion implantation. The yellow dashed lines are a guide to the eye for the approximate locations of the emitter, base, and collector regions. (b) Optical microscopy image of the device array with metal contacts.

FIG. 2. Measured (open symbols) and simulated (lines) photocurrent characteristics of a SiNW \(pnp\) phototransistor with 200-nm nominal base length. (a) Log-linear scale plot of current vs emitter voltage at various illumination intensities (spanning four orders of magnitude) at a wavelength of \(\lambda_0 = 532\) nm. (b) Linear-linear scale plot of current vs emitter voltage for an illumination intensity of 15 W/cm\(^2\). The simulated photocurrent curves assume a base length of 140 nm, carrier mobilities equal to 1/10 of bulk values, and a surface recombination velocity (SRV) of 1000 cm/s.
base region during the dopant activation by a rapid thermal annealing process.

In Fig. 3(a), we plot photocurrent gain vs nominal base length for a range of illumination intensities. Photocurrent gain is defined as the ratio of the photocurrent of the SiNW phototransistor to the photocurrent of a SiNW p+-n diode (See the inset of Fig. 3(a)) with absorption characteristics equivalent to those of the base-collector junction of the phototransistor, under similar illumination and operating bias conditions. For simplicity, the SiNW diode is chosen to be a p+-n structure with 1.5 μm n-region rather than a p+-i-n + structure with 1 μm i-region. Optoelectronic simulations predict that the SiNW p+-n and p+-i-n + diode structures produce similar levels of primary photocurrent (See the supplementary material, Figure S2.2).21 Fig. 3(a) compares the experimental photocurrent gains of a set of SiNW phototransistors with nominal base length ranging from 100 to 500 nm, at different illumination intensities (wavelength 532 nm). Fig. 3(a) also contains a plot of simulated photocurrent gain vs effective base length (solid line), which is 60 nm shorter than the nominal base length (as explained above). The experimental photocurrent gain decreases with increasing base length and begins to saturate at a nominal base length ≥300 nm, i.e., at an effective base length ≥240 nm. This decrease in gain is known to be the result of the degraded base transport factor in phototransistors with wide base regions.26 For a pnp transistor, it means that the fraction of the emitter-injected hole current that reaches the collector is reduced due to recombination with electrons in the base. To minimize the base recombination loss, the nominal base length needs to be ≤100 nm.

Although Fig. 3(a) shows that the device with the narrowest base exhibits the highest photocurrent gain of over 200 at low-intensity illumination, the gain decreases gradually with increasing light intensity. This is examined in more detail in Fig. 3(b), which plots photocurrent vs. illumination intensity of the SiNW phototransistors and the equivalent diode. The unity slope of the photocurrent vs illumination intensity plot (log-log scale) indicates that the photocurrent of the SiNW p+-n diode scales linearly with incident light intensity over a four-decade range, which is an expected characteristic of a photodiode. Similarly, the SiNW phototransistors with wider base (nominally ≥300 nm) also exhibit approximately linear photoresponse with respect to light intensity, albeit with a small gain of <10 over the photoresponse of the p+-n diode. In contrast, the photocurrent vs. light intensity characteristic of the device with 100-nm base becomes sublinear (the slope of the log-log plot is less than one) at intensity levels >100 mW/cm². This can be possibly attributed to the charge screening effect of a large number of holes (from p+ emitter) passing across the base-collector depletion region, which narrows the depletion region and extends the effective base length proportionally. The extension of the base length results in the reduction in gain. This gain reduction is negligible for low-gain devices with longer base length (≥300 nm) but becomes relatively significant for those high-gain devices with shorter base lengths at high illumination intensity (Fig. 3(b)).

In the experiments, a few SiNW phototransistor devices on the sample are found to exhibit exceptionally high photocurrent gains (up to ~5500) relative to a SiNW p+-n diode, as shown in Figs. 4(a) and 4(b). The high gain characteristics of the device are possibly attributable to the annealing-induced narrowing of the base length to ≤40 nm and strain-induced enhancement of carrier mobilities in the nanowire base and collector regions.27,28 The gain characteristics in Fig. 4 have two other distinct features in addition to the exceptionally high values. First, the gain decreases as the light intensity increases (Fig. 4(a)). This is due to the fact that the photocurrent vs light intensity of the nanowire phototransistor shows a sublinear behavior (Fig. 4(b)), resulting in a smaller gain at higher light intensity (larger photocurrent). The sublinearity is possibly caused by the charge screening effect of a large number of holes flowing across the base-collector depletion region, as explained previously. Second, the gain is relatively larger under longer wavelength illumination (Fig. 4(a)), which looks surprising at the first glance. This phenomenon is essentially also related to the photocurrent sublinearity of the device. It is known that silicon is less absorptive at longer wavelength, which results in smaller photocurrent in the device (Fig. 4(b)) although longer wavelength means a higher density of photon flux. A smaller photocurrent at longer wavelength (the same light intensity) leads to a higher gain, according to the photocurrent sublinearity shown in Fig. 4(b).

FIG. 3. (a) Measured photocurrent gain values of SiNW pnp phototransistors at Vg = 1 V vs. nominal base length under 3 mW/cm² (circles), 300 mW/cm² (triangles), and 30 W/cm² (squares) illumination intensities (wavelength λo = 532 nm). The solid line plots simulated photocurrent gain vs. effective base length (60 nm shorter than nominal base length) for 300 mW/cm² illumination intensity. Inset: Photocurrent of SiNW bipolar phototransistor (BPT) and p+-n photodiode under illumination intensity of ~15 W/cm². (b) Measured photocurrent at Vg = 1 V as a function of illumination intensity of SiNW pnp phototransistors with 100–500 nm nominal base lengths, compared with that of a p+-n diode. The dotted lines are a guide to the eye.
The spectral responsivity of the high-gain SiNW phototransistor is measured and plotted as shown in Fig. 5. Measurements are performed with illumination from a grating-based monochromator (HORIBA iHR320) with a focal length of 0.32 m, slit widths of 7 mm, a 250 W tungsten-halogen white-light source, and the appropriate long-pass filters to remove higher-order wavelengths from the output beam. The output beam of the monochromator is approximately collimated and is coupled into a fiber, whose output illuminates the device. A commercial photodiode (Hamamatsu G10899-003K) with a validated spectral responsivity curve (in A/W) is carefully positioned under illumination conditions similar to those used for the SiNW phototransistor to determine the illumination intensity (in W/cm²). The responsivity at each wavelength is calculated by dividing the measured photocurrent by the optical power incident onto the top surface area of the SiNW (i.e., SiNW surface area multiplied by illumination intensity). The resultant responsivity spectrum has a trend of decrease as the wavelength increases (Fig. 5), due to the fact that the silicon nanowire absorbs less light at longer wavelength. Note that the photo gain is equal to the photoresponsivity of the nanowire phototransistor normalized by that of the p⁺-n diode. As a result, the gain is expected to be a constant independent of wavelength. But it actually increases with the wavelength (Fig. 4(a)) due to the sublinearity effect as previously explained. In the spectrum of Fig. 5, there are ripples in photoresponsivity which are the result of enhanced absorption due to optical resonances of the SiNW, as confirmed by simulation (inset in Fig. 5). This leads to a peak responsivity of ~410 A/W at around 380–390 nm, which is three orders of magnitude higher than the typical ultraviolet (UV) responsivity (0.2–0.3 A/W at 400 nm) of commercial bulk Si photodiodes. The near-infrared responsivity of ~10 A/W at 1000 nm is comparable to that of a commercial bulk Si avalanche photodiode (APD) but is achieved at only a small fraction of the bias voltage of an APD (e.g., >100 V for the Si APD of Ref. 31).

In conclusion, the measured optical gain and photoresponse characteristics of our SiNW phototransistors are in good agreement with the predictions of simulations. The devices exhibit good photoresponse linearity over a wide range of illumination intensities. Of noteworthy significance is the ability of the SiNW phototransistor to produce a high gain exceeding 5000, and high responsivity exceeding 100 A/W in the UV and visible ranges, as well as 10 A/W in the near infrared region, at a low operating voltage of 1 V. We believe that the device performance can be enhanced by improving process parameters, in order to achieve higher carrier mobilities and better control of doping profile, especially in the base region. This work has opened up an approach for realizing nanoscale photodetectors, which has shown potential for outperforming conventional photodiodes and phototransistors.

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30. Hamamatsu S12023 series Si APD datasheet.

31. Hamamatsu S5627-01, S4797-01, S6931-01, S2833-01, S4011-06DS Si photodiodes datasheet.

32. Hamamatsu S5627-01, S4797-01, S6931-01, S2833-01, S4011-06DS Si photodiodes datasheet.
Supporting Information

High-Performance Silicon Nanowire Bipolar Phototransistors

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S1. Device fabrication method

SiNW phototransistor devices were fabricated on a silicon-on-insulator (SOI) wafer using a top-down approach. The SOI wafer consists of a (100)-oriented, 200 ± 10 nm thick, boron-doped silicon (Si) device layer with a resistivity of 10-20 Ω cm. The thickness of the oxide layer below the Si device layer is 375 ± 10 nm. Prior to device processing, the wafer was cleaned in piranha (H\textsubscript{2}SO\textsubscript{4}:H\textsubscript{2}O\textsubscript{2}) and buffered oxide etch (NH\textsubscript{4}F:HF) solutions. Arrays of single nanowires were patterned on the wafer by electron beam lithography (EBL). Following EBL, a thin layer of aluminum (Al) film was thermally evaporated on the wafer and lifted off to form a metal etch mask. The nanowires were formed by etching the Al-masked Si device layer down to the top of
the oxide layer, using a SF$_6$/C$_4$F$_8$ inductively coupled plasma reactive ion etching (ICP-RIE) process. Subsequently, the Al mask was removed using standard Al etchant.

Figure S1.1 illustrates the pnp phototransistor device structure, which comprises a 200 nm-wide and 3 µm-long nanowire connected at each end to a large contact pad. The pnp device consists of 1-µm-long $p^+$-doped emitter, $n$-doped base with length ranging from 100 to 500 nm, 1-µm-long $p^-$-doped collector, and 1-µm-long $p'^+$-doped sub-collector. A $p'^+$-doped contact pad is connected to the end of the emitter and sub-collector regions, respectively. Additionally, $p'^+$-$n$ SiNW diodes with dimensions similar to the pnp phototransistors were also fabricated on the same wafer. The $p'^+$-$n$ SiNW diode structure is shown in Figure S1.2. Since the $p'^+$-$n$ diode functions as a unity-gain photodiode at low bias voltages, it provides the baseline from which the photocurrent gain of the pnp SiNW phototransistors can be defined.

![Figure S1.1. Schematic of the pnp SiNW phototransistor device structure. The diagonal lines and grid lines represent the boron ($p^+$-type) and phosphorus ($n$-type) ion implantation windows, respectively.](image)
Figure S1.2. Schematic of the $p^+$-$n$ SiNW diode structure which serves as a unity-gain device in this work. The diagonal lines and grid lines represent the boron ($p^+$-type) and phosphorus ($n$-type) ion implantation windows, respectively.

The doping of the $p^+$ and $n$-type active regions of the SiNW devices fabricated in this work was defined, respectively, by boron and phosphorus ion implantation. The energy and dose values are 20 keV, $8 \times 10^{14}$ B$^+$ cm$^{-2}$ for boron, and 80 keV, $1 \times 10^{13}$ P$^+$ cm$^{-2}$ for phosphorus. Each of the two ion implantation steps was performed separately, following cleaning and EBL patterning of implantation windows on the wafer with polymethylmethacrylate (PMMA) resist. The PMMA resist has sufficient density and thickness to act as a robust mask. To correct for possible EBL alignment error (estimated to be <100 nm), the base ion implantation window was extended by 100 nm into the emitter region. The overlapped region has negligible effect on the emitter-base junction profile as the emitter region is significantly more highly doped than the base region.
Figure S1.3. Boron (solid line) and phosphorus (dotted line) ion-implanted doping profiles measured by SIMS following RTA annealing of as-implanted sample. The depth value of 0 represents the top surface.

Following ion implantation, both the boron and phosphorus dopants were activated simultaneously by rapid thermal annealing (RTA) at 1000 °C in an N₂ ambient. Figure S1.3 shows the secondary ion mass spectroscopy (SIMS) profiles of the annealed boron and phosphorus ion-implanted regions. The average doping concentration in the $p^+$ (emitter, sub-collector and contact) and $n$-type (base) regions is approximately $5 \times 10^{19}$ cm⁻³ and $5 \times 10^{17}$ cm⁻³, respectively. Finally, Ti/Au ohmic contacts were thermally evaporated on the emitter and sub-collector contact pad windows of the devices.
S2. Simulation method

In order to compare the experimental device performance with theoretical prediction, the electro-optical characteristics of the $pnp$ SiNW phototransistor were simulated using the Lumerical DEVICE and FDTD software packages. A schematic of the finite-difference time-domain (FDTD) simulation structure is shown in Figure S2.1, which consists of a SiNW top-illuminated by a plane-wave source. Perfectly-matched-layer (PML) boundary conditions are used in the $z$ direction (normal to the top absorbing surface of the SiNW), while periodic boundary conditions are defined in the $x$ (NW length) and $y$ (NW width) directions. The FDTD simulation produces the photo-generation profile which was incorporated into the electrical simulation performed with Lumerical DEVICE.

In the simulated $pnp$ SiNW device structure, the doping concentration of the $p$-doped collector region is equivalent to $1 \times 10^{15} \text{ cm}^{-3}$, as estimated from the resistivity value of the silicon device.
layer of the SOI wafer, and assumed to be uniform in all directions. The doping concentrations of the $p^+$-doped emitter, sub-collector and contact regions and the $n$-doped base region are assumed to be uniform in the $x$ and $y$ directions and varying in the $z$ direction according to the doping profiles given in Figure S1.3. Bulk (Shockley-Read-Hall) and surface recombination mechanisms are included in the electrical simulation. In addition, $p^+-i-n^+$ and $p^+-n$ SiNW diode structures with, respectively, $i$-region ($p^-$-doped at $1 \times 10^{15}$ cm$^{-3}$) and $n$-region ($n$-doped at $5 \times 10^{17}$ cm$^{-3}$) length comparable to the collector length of the $pnp$ SiNW phototransistor, were also simulated for comparison with the $pnp$ device.

**Figure S2.2.** Simulated photocurrents in a SiNW phototransistor (100-nm base, 1-μm collector) with only the base, collector, or the whole SiNW active region under illumination. Also shown are simulated photocurrents of $n^+-i-p^+$ diode with 1 μm $i$-region and $n-p^+$ diode with 1.5 μm $n$-region, for comparison. The incident light is 532 nm at ~160 W cm$^{-2}$. 
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